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APPLICATION NO.	FILI	NG DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
08/602,503	. 02/20/1996		MICHAEL B. BALL	2718US	4539
7:	590	05/12/2003			
JOSEPH A WALKOWSKI TRASK BRITT & ROSSA PO BOX 2550			EXAMINER		
				NGUYEN, DILINH P	
SALT LAKE CITY, UT 84110			ART UNIT	PAPER NUMBER	
		•		2814	

DATE MAILED: 05/12/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	
•	•	08/602,503	BALL, MICHAEL B.	
·	Office Action Summary	Examiner	Art Unit	
		DiLinh Nguyen	2814	
	The MAILING DATE of this communication ap			
THE	OF REPLY  ORTENED STATUTORY PERIOD FOR REPL  MAILING DATE OF THIS COMMUNICATION.  Insigns of time may be available under the provisions of 37 CFR 1.1	_	. ,	
after - If the - If NC - Failu - Any r	SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period ree to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing patent term adjustment. See 37 CFR 1.704(b).	ly within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a, cause the application to become ABANDONE	ys will be considered timely. In the mailing date of this communication. ID (35 U.S.C. § 133).	
Status				
1)🛛	Responsive to communication(s) filed on <u>06</u>			
2a)⊠	,—	nis action is non-final.		
3) 🗌 Disposit	Since this application is in condition for allow closed in accordance with the practice under ion of Claims			
•	Claim(s) <u>19,21-23 and 25-34</u> is/are pending i	n the application.		
	4a) Of the above claim(s) is/are withdra			
	Claim(s) is/are allowed.			
·	Claim(s) <u>19,21-23 and 25-34</u> is/are rejected.			
·	Claim(s) is/are objected to.			
•	Claim(s) are subject to restriction and/o	or election requirement.		
•	ion Papers	·		
9)	The specification is objected to by the Examine	er.		
10)	The drawing(s) filed on is/are: a)□ acce	pted or b)⊡ objected to by the Exa	miner.	
	Applicant may not request that any objection to the	ne drawing(s) be held in abeyance. S	See 37 CFR 1.85(a).	
11)	The proposed drawing correction filed on	_ is: a)□ approved b)□ disappro	oved by the Examiner.	
	If approved, corrected drawings are required in re	eply to this Office action.		
12)	The oath or declaration is objected to by the Ex	xaminer.		
Priority (	under 35 U.S.C. §§ 119 and 120			
13)	Acknowledgment is made of a claim for foreig	n priority under 35 U.S.C. § 119(a	a)-(d) or (f).	
a)	☐ All b)☐ Some * c)☐ None of:			
	1. Certified copies of the priority documen	ts have been received.		
	2. Certified copies of the priority documen	ts have been received in Applicat	ion No	
* (	3. Copies of the certified copies of the price application from the International Bosee the attached detailed Office action for a list	ureau (PCT Rule 17.2(a)).		
	Acknowledgment is made of a claim for domest	·		
· ·	a)   The translation of the foreign language pr			
	Acknowledgment is made of a claim for domes			
Attachmer		·		
2) Notic	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal	ry (PTO-413) Paper No(s) Patent Application (PTO-152)	
I.S. Patent and	Trademark Office			

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#### **DETAILED ACTION**

### Claim Objections

Claim 19 is objected to because of the following informalities:

The phrase: "... at least one active face-up stack die **one** the layer of conductive epoxy adhesive..." should be changed to active face-up stack die on the layer of conductive epoxy adhesive.

Appropriate correction is required.

## Response to Amendment

The amendment filed 3/6/03 is objected to under 35 U.S.C. 132 because it introduces new matter into the disclosure. 35 U.S.C. 132 states that no amendment shall introduce new matter into the disclosure of the invention. The added material which is not supported by the original disclosure is as follows: electrically connecting said at least one stack die directly to at least one of said conductors.

Applicant is required to cancel the new matter in the reply to this Office Action.

#### Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claim 19 is rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one

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skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

Electrically connecting said at least one stack die directly to at least one of said conductors is new subject matter. How is the stack die directly connecting to at least one of the conductors?

The specification and drawings disclose the stack die is connecting to the adhesive layer 28 and the lower chip 14; wherein the chip 14 is directly connecting to the conductors 20.

# Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yamauchi (JP.Pat. 5-13665) in view of Schonhorn et al. (U.S. Pat. 4377619)

Yamauchi discloses a method of fabricating a multi-die assembly (Fig. 1, abstract) comprising:

providing a subsbtrate 1 including a plurality of conductors 3;

attaching at least one active face down base die to the substrate in electrical communication with at least some of the plurality of conductors;

providing a layer of adhesive to a back side of the at least one base die;

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placing a back side of at least one active face up stack die on the layer of adhesive;

securing the back side of at least one stack die to the at least one base die;
electrically connecting at least one stack die to at least one of the conductors,
and electrically grounding at least one base die via the adhesive and at least one stack
die.

Yamauchi fails to disclose the adhesive is the conductive epoxy adhesive and curing the layer of conductive epoxy adhesive.

Schonhorn et al. disclose a semiconductor device comprising: a layer of conductive epoxy adhesive on the substrate; placing a chip on the layer conductive epoxy adhesive and curing the layer of conductive epoxy adhesive (cover fig., column 6, lines 1-7). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Yamauchi to prevent metal migration and to secure more firmly the chip to the substrate, as shown by Schonhorm et al.

3. Claims 21-23, 25-29 and 33-34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamauchi (JP.Pat. 5-13665) in view of Schonhorn et al. and further in view of Fogal et al. (U.S. Pat. 5323060).

Yamauchi and Schonhorn et al. fail to disclose at least another stack die to the assembly and securing at least one discrete component to at least one of the stack die, the base die and the substrate.

Regarding claims 23 and 25, Fogal et al. discloses a multi-chip semiconductor
 (fig. 1, column 2, lines 35 et seq.) comprising: securing at least another stack die
 54 to the assembly; and securing the stack die 54 to at least one of the stack die
 28 to provide a wire bonding of multichip modules to multichip module substrate.

- Regarding claims 21-22, 26-29 and 33-34, Fogal et al. disclose a multichip module (fig. 5, column 3, lines 43 et seq.) comprising:
   a discrete component 75 to the substrate 12;
- a discrete components 76 and 78 to an adhesive layer 77 to an upper uppermost chip 85; and

a bond wires 44a, 44b, and 79-81, wherein the bond wires bonding to the substrate and the chips. Fogal et al. show that discrete components can be added, while it is not specifically pointed out, the discrete component could include a filer (by pass) capacitor (column 3, line 53) which is needed for proper device operation and is not normally formed as part of a chip. It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Yamauchi and Schonhorn et al. to provide additional necessary components.

4. Claims 30-32 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamauchi (JP.Pat. 5-13665) in view of Schonhorn et al. and Fogal et al. (U.S. Pat. 5323060) and further in view of Rostoker (U.S. Pat. 5399898).

Yamauchi, Schonhorn et al. and Fogal et al. disclose the claimed invention except for not further disclose the face down base die includes attaching at least two active face down base die to the substrate.

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Rostoker discloses the attaching at least one active face down base die includes attaching at least two active face down base die 404 and 410 (fig. 4a, column 14, lines 40 et seq.) to the substrate 402 and electrically coupling each of the base die with one of the plurality substrate conductors 408 and 414; and

a bridging 416 at least one stack die between the two base die, the assembly is forming should that none of the dice are arranged face to face and limited overlap. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Yamauchi, Schonhorn et al. and Fogal et al. to provide a greater power dissipation and a natural convection cooling channel, as shown by Rostoker.

### Response to Arguments

Applicant's arguments with respect to claim 19, 21-23 and 25-34 have been considered but are moot in view of the new ground(s) of rejection. Therefore, see the new rejection above.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (703) 305-6983. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (703) 308-4918. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7724 for After Final communications.

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

DLN May 6, 2003

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